

BEST AVAILABLE COPY

I Number	Hits	Search Text	DB	Time Stamp
83	6	symmetric nears ("multi-processor" multiprocessor) near10 (die package integrated)	USPRT; US-PCPUB; EPO; JPO; DERMENT; IBM TDB	2003/12/30 11:57
84 85	0 25	20030120963.URPN. (smp symmetric nears ("multi-processor" multiprocessor)) near10 embedded	USPRT; US-PCPUB; EPO; JPO; DERMENT; IBM TDB	2003/12/30 11:56 2003/12/30 11:58
86	100	("multi-processor" multiprocessor) near10 embedded	USPRT; US-PCPUB; EPO; JPO; DERMENT; IBM TDB	2003/12/30 11:58
87	0	("US6587926B2").RN.	USPRT; US-PCPUB; EPO; JPO; DERMENT; IBM TDB	2003/12/30 13:53
88	14	("6587926") or ("6571322") or ("20020184546") or ("6560682") or ("6418460") or ("20020184450") or ("6574142").RN.	USPRT; US-PCPUB; EPO; JPO; DERMENT; IBM TDB	2003/12/30 14:04
89	2	("20030023794").RN.	USPRT; US-PCPUB; EPO; JPO; DERMENT; IBM TDB	2003/12/30 14:04
-	89	smp nears (die package circuit)	USPRT; US-PCPUB; EPO; JPO; DERMENT; IBM TDB	2003/12/30 11:33
-	5	cache & (smp nears (die package circuit))	USPRT; US-PCPUB; EPO; JPO; DERMENT; IBM TDB	2003/12/30 11:32
-	31	smp nears (die package integrated)	USPRT; US-PCPUB; EPO; JPO; DERMENT; IBM TDB	2003/12/30 11:54



[Advanced Search](#)
[Preferences](#)
[Language Tools](#)
[Search Tips](#)

Web · Images · Groups · Directory · News ·
Searched the web for simp "cache coherent".
Results 1 - 10 of about 4,740. Search took 0.14 seconds.

Sponsored Link
Coherent - Buy Books and More on eBay
www.eBay.com eBay - The World's Online Marketplace

... links [12] To match this bus bandwidth, SMP designers have increased snoop ... G
White and P. Vogt, Profusion (tm): A Buffered, Caching Coherent Crossbar Switch ...
classer.nij.nec.com/context/709987/0 - 11k - Cached - Similar pages

Citations: Scaling application performance on a cache-coherent ...

Proc. ... D. Jiang and J.P. Singh. Scaling Application Performance on Cache-Coherent Multiprocessors
 Proc. ... Barner Synchronization on a Loaded SMP using Two-Phase. ...
 classes/nj.nec.com/conext89/0902253719 - 31k - [Cached - Similar pages](#)
[\[More results from classes.nj.nec.com \]](#)

IPCOM000018762D: Method for a cache-coherent I/O domain
 ... General description. The disclosed method is a cache-coherent I/O domain that provides SMP characteristics to intelligent I/O subsystems. ...
www.priorartdatabase.com/IPCOM000018762/ - [Similar pages](#)

Cache Coherent Non Uniform Memory Access (ccNUMA) Support

... speed cache coherent system interconnect to create a larger system. Access from a processor to node local memory is fast, as is the case with small SMP systems ...

Optimizing Software Cache-coherent Cluster Architectures

... In this paper, we explore a spectrum of latency hiding techniques in the context of cache-coherent parallel systems where each node is an SMP cluster. ...

www.supercomp.org/sc98/TechPapers/sc98_FullAbstracts/Qin753/-54k-Cached-Similar-pages

PPN Achieving Multiprogramming Scalability of Parallel Programs on ...

... Compaq, hardware cache-coherent SMPs, software cache-coherent DSM systems built from NOVs. Scalability up to a few hundreds of processors. ...

www.cs.wm.edu/~dsn/presentations/hci99.ppt - Similar pages

Helix Systems Overview

... Cluster. Architecture. ccNUMA (cache coherent non-uniform memory access),

[helix.nih.gov/systems/](#) - 13k - Cached - Similar pages

SC: Supercomputing '98. Optimizing software cache-coherent ...

... Software cache-coherent systems using programmable protocol processors provide a flexible infrastructure to ... of a shared-memory system consisting of SMP clusters ...

Similar pages

Optimizing Software Cache-coherent Cluster Architectures

... Software cache-coherent systems using programmable protocol processors provide a flexible infrastructure ... of a shared- memory system consisting of SMP clusters. ... csdl.computer.org/comp/proceedings/sc/1998/8707/00/8700025aabs.htm - 12k - [Cached](#) - [Similar pages](#)

[pdf] Cache-coherent Distributed Shared Memory: Perspectives On Its ...
[File Download](#) PDF of the Article View as HTML

Unfortunately, the above schemes used in some of the

... Unfortunately, the snoopy schemes used in small-scale SMP's do not ... [ming approach](#)
used for the widespread small-scale, cache-coherent multiprocessors and the ...
cva.stanford.edu/cs99s/papers/finemessy-cc.pdf - [Similar pages](#)

Go0000000000g|e▶
Result Page: 1 2 3 4 5 6 7 8 9 10 Next

Next

Google Search

Search within results

Dissatisfied with your search results? Help us improve

Get the Google Toolbar



Google Home - Advertise with Us - Business Solutions - Services & Tools - Jobs, Press, & Help

©2003 Google

BEST AVAILABLE COPY

Digitized by Google

Printed 12/30/03 (10:44:37 AM)

Google Home - Advertise with Us - Business Solutions - Services & Tools - Jobs, Press, & Help

©2003 Google

BEST AVAILABLE COPY

Printed 12/30/03 (10:44:37 AM)